

Amendments to the Drawings:

Attached to this Amendment are Replacement Sheets for Figures 1-8.

REMARKS

Reconsideration and allowance of the subject application in view of the foregoing amendments and the following remarks is respectfully requested.

Claims 2, 3, 7, 8, 12, 13, 17 and 18 remain pending in the application. Claims 2, 3, 7, 8, 13, 17 and 18 have been amended.

The specification has been amended to include one or more claims as suggested by the Examiner.

The drawings stand objected to as noted in the Office Action. Attached are Replacement Figures 1-8 showing proper figure designations. Therefore, this objection should be withdrawn.

Claims 2, 3, 7, 8, 12, 13, 17, 18 are rejected to because of the noted informalities. In response, the preambles of these claims have been amended and this objection should be withdrawn.

Claims 2, 3, 7, 8, 12, 13, 17 and 18 are rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential structural cooperative relationship of elements, such omission amounting to a gap between the necessary structural connections. Applicant respectfully traverses this rejection.

Referring to the second embodiment and Figure 6, amended claim 2 is explained below. There is a cooperative relationship between “a timing generating means operative, when the collation and synchronism decision means gives a decision for inconsistency in phase, for a match between the synchronism pattern detecting timing recorded in the synchronism pattern detecting timing recording means, as a subsequent one, and a timing of a synchronism pattern of the expectation data.”

With respect to a synchronism pattern detecting timing recording means, a plurality of synchronism patterns has a first of the plurality of synchronism patterns (e.g., a false synchronism pattern 40 or 42) and a second of the plurality of synchronism patterns (e.g., a false synchronism pattern 42 or a (true) synchronism pattern 30).

With respect to a collation and synchronism decision means, a collator 10 performs a collation between input reception data and expectation data (page 14, lines 17-19).

With respect to “wherein..., and wherein...is detected”, referring to page 22, first

paragraph and Fig. 6(a), the first of the plurality of synchronism patterns is the false synchronism pattern 40. The second of the plurality of synchronism patterns is the false synchronism pattern 42. No synchronism between the reception data and the expectation data are obtained (i.e., phases are inconsistent). The second of the plurality of synchronism patterns (the false synchronism pattern 42) is recorded as the first of the plurality of synchronism patterns.

Referring to page 22, second paragraph and Figure 6(c), the first of the plurality of synchronism patterns is the false synchronism pattern 42. The second of the plurality of synchronism patterns is the (true) synchronism pattern 30. No synchronism between the reception data and the expectation data are obtained (i.e., phases are inconsistent). The second of the plurality of synchronism patterns (the (true) synchronism pattern 30) is recorded as the first of the plurality of synchronism patterns.

Referring to page 22, third paragraph and Figure 6(e), the first of the plurality of synchronism patterns is the (true) synchronism pattern 30. Synchronism between the reception data and the expectation data are obtained (i.e., phases are consistent). Thus, it should be apparent that there are structural relationships between elements and according, this rejection should be withdrawn.

Claims 2, 3, 7, 8, 12, 13, 17 and 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Koishi et al. (U.S. Patent No. 5,463,639). Applicant respectfully traverses this rejection.

According to the present invention, the input reception data and the expectation data contain a common synchronism pattern. The synchronism pattern contained in the input reception data is detected. The expectation data is generated in accordance with the detected timing. The synchronism between the reception data and the expectation data is obtained (page 2, first paragraph).

However, the input reception data also contains the false synchronism pattern, which is not a synchronism pattern, but has like data to the synchronism pattern. Therefore, no synchronism between the reception data and the expectation data is obtained when the false synchronism pattern contained in the input reception data is detected (page 2, second paragraph).

According to the present invention (page 22, first and third paragraph and Figure 6), the synchronism pattern detecting timing recording means records the second synchronism timing (pattern) as the first synchronism timing (pattern) when a decision for inconsistency in phase is

given (i.e., when the false synchronism pattern contained in the input reception data is detected). Therefore, the synchronism between the reception data and the expectation data is obtained even if the false synchronism pattern contained in the input reception data is detected.

Koishi discloses that a reference-pattern-generating circuit 15 generates a reference pattern output which is delayed by one bit until an error pulse 36 does not occur (col. 3, lines 38-49 and Fig. 7). Furthermore, Koishi discloses that pulses are delayed by a variable delay part 14 and is sent to an error counter 18 so that a mark ratio is defined (col. 8, line 66 – col. 9, line 1 and Fig. 1).

Koishi does not disclose that the synchronism pattern is contained in the input reception data. Therefore, Koishi does not disclose that the synchronism pattern and the false synchronism pattern contained in the input reception data are detected. Therefore, Koishi does not disclose the synchronism pattern detecting timing recording means of the present invention by which the synchronism between the reception data and the expectation data is obtained even if the false synchronization pattern contained in the input reception data is detected. Furthermore, Koishi does not disclose that the synchronism pattern detecting timing recording means of the present invention, which records the second synchronism timing (pattern) as the first synchronism timing (pattern) when a decision for inconsistency in phase is given.

Koishi does not disclose the synchronism pattern detecting timing recording means of the present invention. Therefore, this rejection should be withdrawn.

All objections and rejections having been addressed, it is respectfully submitted that the present application should be in condition for allowance and a Notice to that effect is earnestly solicited.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including

extension of time fees, to Deposit Account 07-1337 and please credit any excess fees to such deposit account.

Respectfully submitted,

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A handwritten signature in black ink that reads "Kenneth M. Berner". The signature is written in a cursive, flowing style.

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